

FIGURE 1

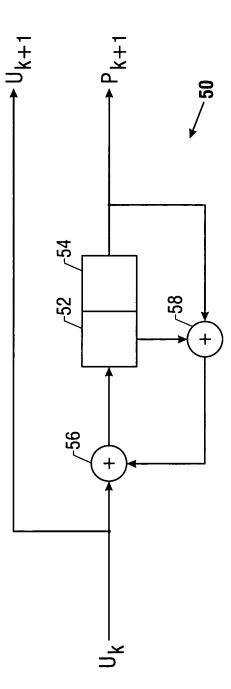


FIGURE 2

state k+1 (shift regs)	00	10	10	00	7	10	10	11
code word Uk+1 Pk+1	00	10	00	10	10	11	10	1
U _k input	0	-	0	-	0	-	0	-
state k (shift regs)	00	00	01	10	10	10	-	7

state transition table 60

FIGURE 3

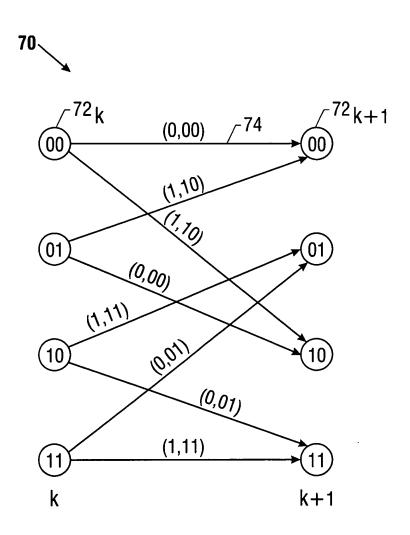
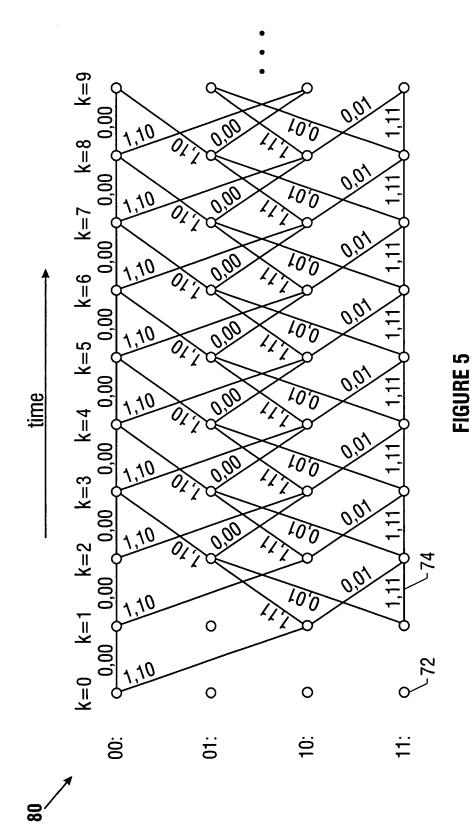


FIGURE 4



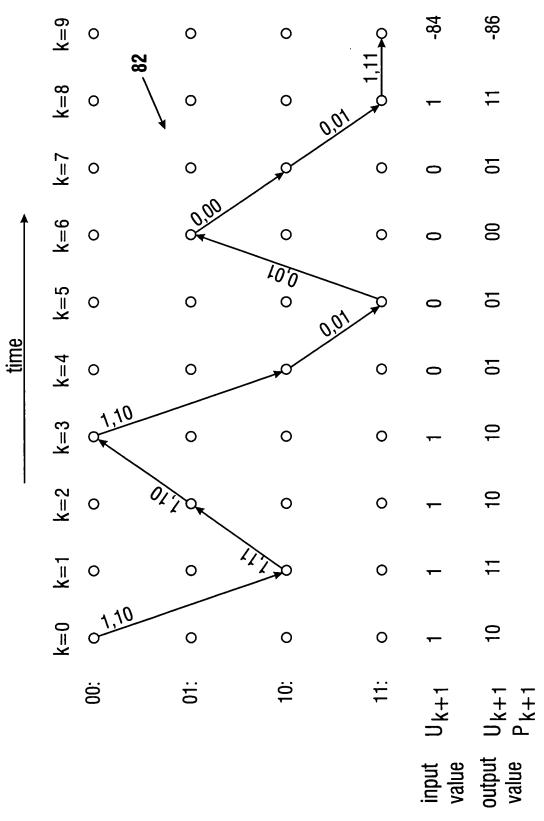


FIGURE 6

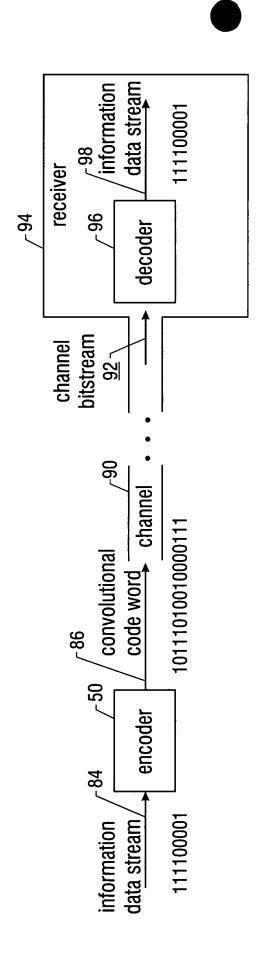


FIGURE 7

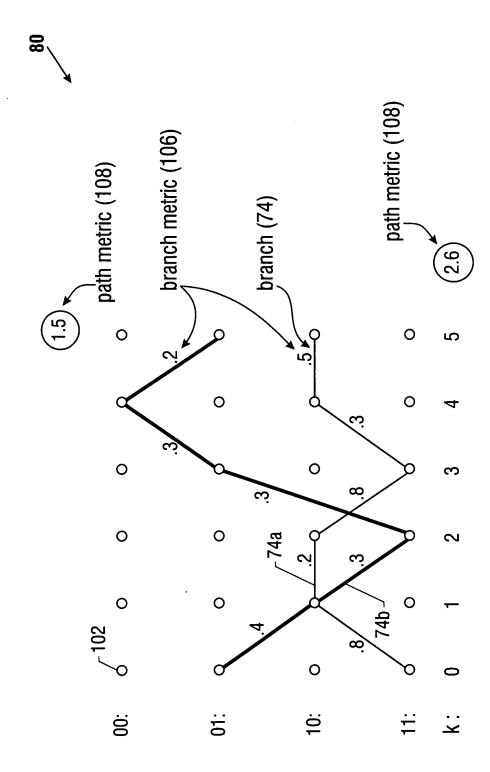


FIGURE 8

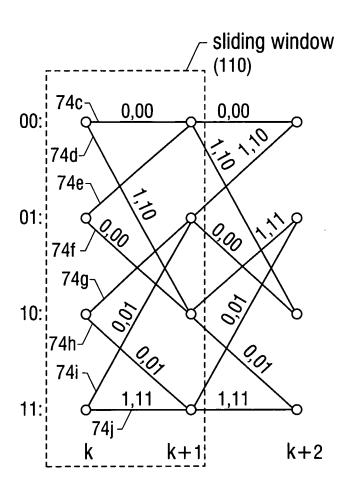
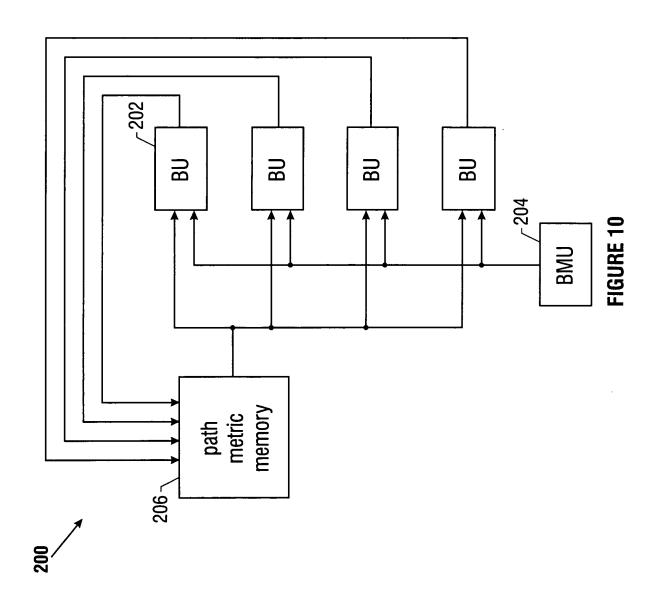
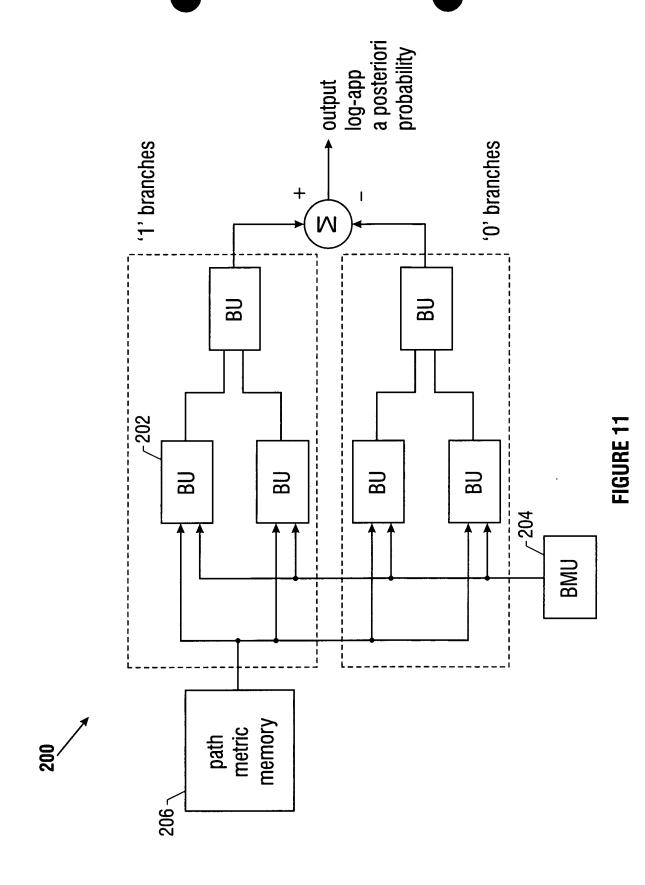


FIGURE 9





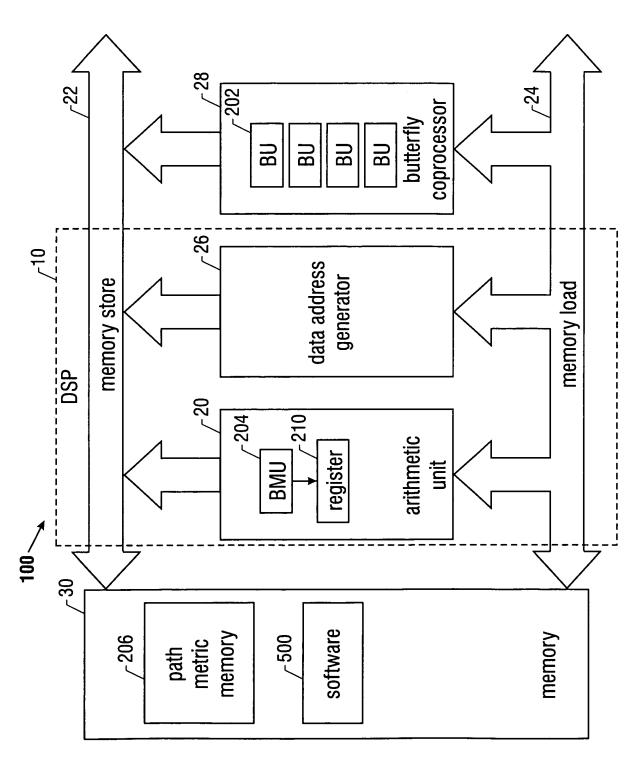


FIGURE 12

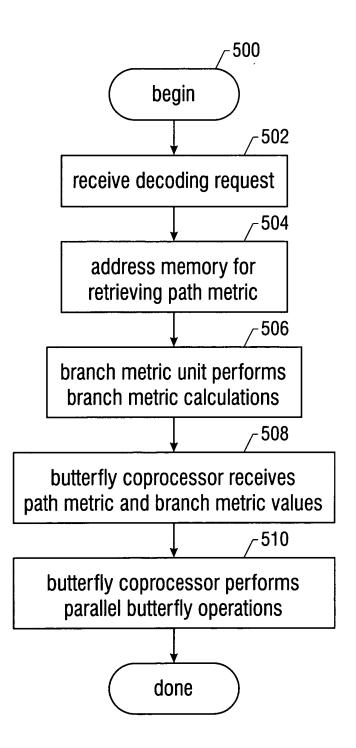


FIGURE 13

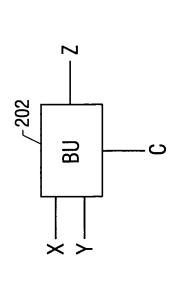


FIGURE 14A

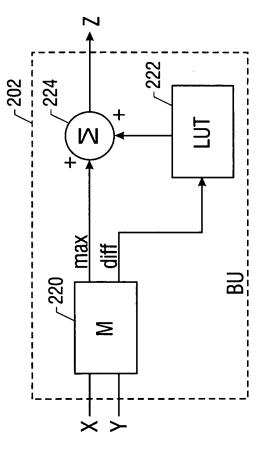
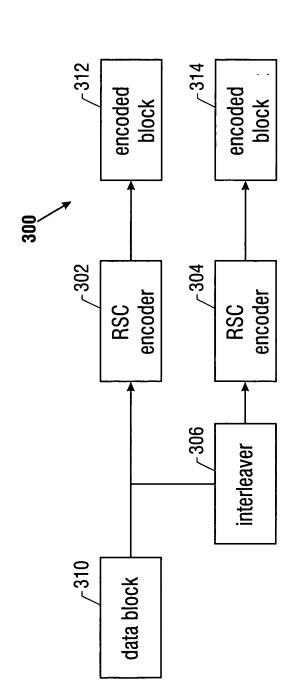


FIGURE 14B



TURBO ENCODER

FIGURE 15

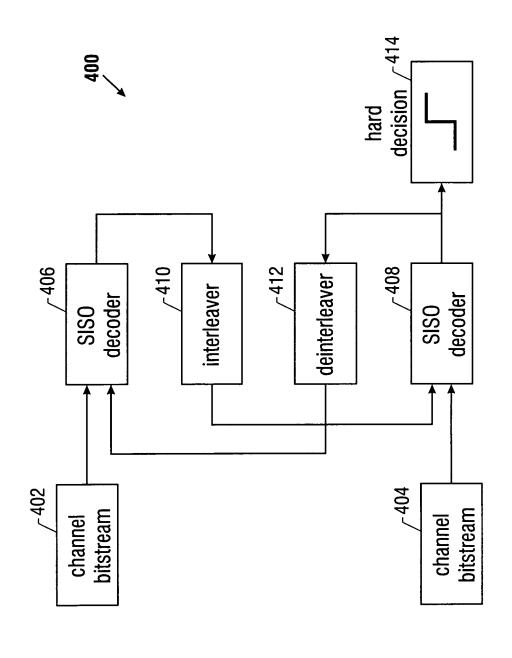


FIGURE 16